

ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention

SYSTEM AND METHOD FOR ARBITRATION BETWEEN SHARED
PERIPHERAL CORE DEVICES IN SYSTEM ON CHIP
ARCHITECTURES

Submission Type : Utility Patent Filing

Application Number:

10/711084



EFS ID:

67076

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First Named Applicant: Serafino Bueti

Attorney Docket Number: BUR920040148US1

Timestamp: 2004-08-20 16:00:55 EDT

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us-fee-sheet	BUR920040148US1-usfees.xml	2014	2004-08-20
us-fee-sheet	us-fee-sheet.xml	24912	2004-08-20
us-fee-sheet	us-fee-sheet.dtd	11069	2004-08-20
us-ids	BUR920040148US1-usidst.xml	2590	2004-08-20
us-ids	us-ids.dtd	7763	2004-08-20
us-ids	us-ids.xml	12026	2004-08-20
us-assignment	BUR920040148US1-usassn.xml	2730	2004-08-20
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us-assignment	us-assignment.dtd	10898	2004-08-20
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us-assignment	BUR920040148US1Assign2.tif	30164	2004-08-20
us-request	BUR920040148US1-usrequ.xml	4116	2004-08-20
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us-declaration	BUR920040148US1Dec1.tif	79056	2004-08-20
us-declaration	BUR920040148US1Dec2.tif	65662	2004-08-20
us-declaration	BUR920040148US1Dec3.tif	55416	2004-08-20
application-body	BUR920040148US1SPEC-trans.xml	28784	2004-08-20
application-body	us-application-body.xml	83497	2004-08-20
application-body	application-body.dtd	49498	2004-08-20
application-body	wipo.ent	4956	2004-08-20
application-body	mathml2.dtd	54588	2004-08-20
application-body	mathml2-qname-1.mod	13225	2004-08-20

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application-body	mmialias.ent	38209	2004-08-20
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application-body	BUR920040148US1drawing2of3.tif	26436	2004-08-20
application-body	BUR920040148US1drawing3of3.tif	30192	2004-08-20
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package-data	BUR920040148US1-pkda.xml	9195	2004-08-20
package-data	package-data.dtd	27025	2004-08-20
package-data	us-package-data.xsl	19263	2004-08-20
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